

REMARKS

Claims 1-7 and 10-30 remain in the application for consideration of the Examiner with Claims 8 and 9 standing cancelled.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

More particularly, the Examiner objected to the drawings due to a lack of a structure detail that is essential for proper understanding of the disclosed invention.

Applicants respectfully traverse the objection based on the fact that Applicants have not defined essential details and consequently it would appear that the Examiner has reached these conclusions.

It is respectfully submitted that the drawings are in full compliance with 37 CFR 1.83.

Claims 1-3, 7, 12, 13, and 20-30 were rejected under 35 U.S.C. § 112, first paragraph.

The Examiner alleges that there is no enablement for the base clock divided by a composite divisor to produce a baud clock.

Applicants suggest that a baud clock signal is produced and claimed and not a baud clock.

Furthermore, Applicants direct the attention of the Examiner to page 7, line 5 et seq of the instant specification.

Claims 1-3, 7, 12, 13, and 20-30 are in full compliance with 35 U.S.C. § 112.

Turning now to the art rejections, Claims 6, 8, 9, and 16 were rejected under 35 U.S.C. § 103 as being unpatentable over Loyer.

These rejections are respectfully traversed.

It is respectfully submitted that Loyer does not disclose or suggest the presently claimed invention including the step of providing for each of the possible values of the first divisor component a polarity of possible values of the second divisor component in independent Claim 6.

The Examiner states that Loyer does not disclose programmable over sampling factor.

However, Applicants agree with the Examiner.

Furthermore, it is not within the level of one of ordinary skill in the art to provide the details as recited above.

It is respectfully submitted that Loyer does not disclose or suggest the presently claimed invention including the divisor generator including the lookup table.

Applicants agree with the Examiner that Loyer does not disclose this. However, Applicants respectfully submit that one of ordinary skill in the art would not find this obvious.

Concerning Claims 6 and 16, Applicants respectfully request a teaching from the prior art.


Applicants appreciate the indication that Claims 4, 5, 10, 11, 14, 15, and 17-19 are allowed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,


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